



Docket No.: M4065.0489/P489
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Donald L. Yates, et al.

Application No.: 10/022,721

Confirmation No.: 2293

Filed: December 20, 2001

Art Unit: 2836

For: METHOD OF IMPROVING SURFACE
PLANARITY PRIOR TO MRAM BIT
MATERIAL DEPOSITION

Examiner: Not Yet Assigned

RESPONSE TO RESTRICTION REQUIREMENT


Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed August 15, 2003 (Paper No. 0803), Applicant hereby elects claims 1-21, drawn to a process for making semiconductor devices, for continued examination without traverse. Examination of the elected claims is solicited.

Dated: August 21, 2003

Respectfully submitted,

By 
Thomas J. D'Amico
Registration No.: 28,371
DICKSTEIN SHAPIRO MORIN &
OSHINSKY LLP
2101 L Street NW
Washington, DC 20037-1526
(202) 785-9700
Attorney for Applicant